# **AN5036**

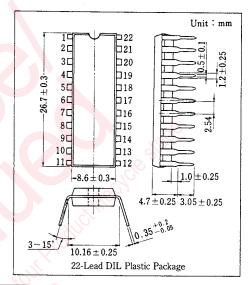
## TV Tuning Control Circuit

#### Outline

The AN5036 is an integrated circuit designed for tuner control circuit of TV electronic tuning system using a frequency synthesizer method.

#### Features

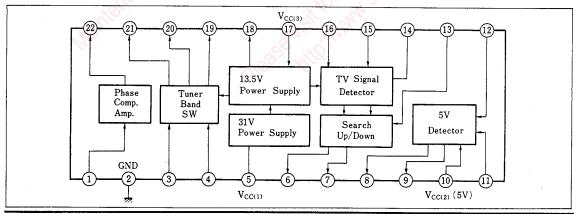
- Reference voltage stabilizer for electronic tuning incorporated
- Electronic tuner power supply incorporated
- Power supply voltage rise—up detection circuit incorporated



#### Pin

Pin No.	Pin Name	Pin No.	Pin Name
1	Phase Comp. Signal Input	12	V <sub>cc2</sub> Det. Input
2	GND	13	AFC. Voltage Input
3	Band SW Input (1)	14	Filter Terminal
4	Band SW Input (2)	15	H. BLK Pulse Input
5	31V Regulator (Vcc1)	16	H. Sync. Input
6	AFC Down Output	17	V <sub>CC3</sub>
7	AFC Up Output	18	BM Output
8	Reset Output	19	BU Output
9	C.E. Output	20	BV Output
10	Vcc2	21	BS Output
11	3V Ref. Voltage Input	22	BT Voltage Output

### Block Diagram



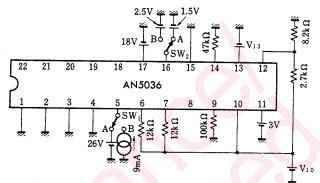
## ■ Absolute Maximum Ratings (Ta=25°C)

Item			Symbol	Rat	ing	Unit	
Voltage	V <sub>cc2</sub>		V <sub>10-2</sub>	7		V	
	Supply Voltage	V <sub>cc3</sub>	V <sub>17-2</sub>	24		V	
voltage	Circuit Voltage		V <sub>6-2</sub> , V <sub>7-2</sub> 0		+ 7	V	
			$V_{21-2}$ 0 +27		+27	V	
	Supply Current		$I_5$	0	+13	mA	
	Circuit Current		I <sub>17</sub>	0	+60	mA	
Current			$I_{18}$	-60	+20	mA	
			$I_{19}, I_{20}$	-30	0	mA	
			$I_{21}$	0	+30	mA	
Power Dissipa	Power Dissipation		P <sub>D</sub>	1100		mW	
Temperature	Operating Ambient Temperature		$T_{opr}$	-20~	-+70	ъ	
	Storage Temperature		$T_{stg}$	-55~+150		r	

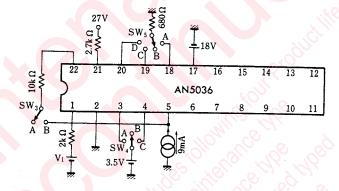
## ■ Electrical Characteristics (Ta=25°C)

Item	Symbol	Test Circuit	Condition	min.	typ.	max.	Unit
V <sub>cc1</sub> Circuit Current	$I_{5}$	1	$S_1=A$ , $S_2=A$ , $V_{10}=5V$ , $V_{13}=3.5V$	2.5	3.3	4.1	mΑ
V <sub>CC2</sub> Circuit Current	I <sub>10</sub>	1	$S_1=B, S_2=A, V_{10}=5V, V_{13}=5V$	4.2	5.6	7.2	mA
V <sub>CC3</sub> Circuit Current	I <sub>17</sub>	1	$S_1=B$ , $S_2=A$ , $V_{10}=5V$ , $V_{13}=5V$	14	19	24	mA
	$V_{6-2(1)}$	1	$S_1=B$ , $S_2=A$ , $V_{10}=5V$ , $V_{13}=3.5V$	0	0.2	0.4	V
SD Output Voltage	$V_{6-2(2)}$	1	$S_1=B$ , $S_2=A$ , $V_{10}=5V$ , $V_{13}=5.5V$	4.9	5.0	5.1	V
3D Output Voltage	$V_{6-2(3)}$	1	$S_1=B$ , $S_2=B$ , $V_{10}=5V$ , $V_{13}=7.5V$	0	0.2	0.4	V
	$V_{\delta-2(4)}$	1	$S_1=B$ , $S_2=B$ , $V_{10}=5V$ , $V_{13}=9.5V$	4.9	5.0	5.1	V
	V <sub>7-2(1)</sub>	1	$S_1=B$ , $S_2=B$ , $V_{10}=5V$ , $V_{13}=3.5V$	4.9	5.0	5.1	V
SH Outsut Walters	$V_{7-2(2)}$	1	$S_1=B$ , $S_2=B$ , $V_{10}=5V$ , $V_{13}=5.5V$	0	0.2	0.4	V
SU Output Voltage	V <sub>7-2(3)</sub>	1	$S_1=B$ , $S_2=A$ , $V_{10}=5V$ , $V_{13}=7.5V$	4.9	5.0	5.1	V
	V <sub>7-2(4)</sub>	1	$S_1=B, S_2=A, V_{10}=5V, V_{13}=9.5V$	0	0.2	0.4	V
5V Power Supply	V <sub>8-2(1)</sub>	1	$S_1=B$ , $S_2=A$ , $V_{10}=3.5V$ , $V_{13}=5V$	0	0.1	0.3	V
5 V Power Supply	V <sub>8-2(2)</sub>	1	$S_1=B, S_2=A, V_{10}=4.5V, V_{13}=5V$	4.2	4.4	4.5	· V
Rise-up Detection	V <sub>9-2(1)</sub>	1	$S_1=B$ , $S_2=A$ , $V_{10}=3.5V$ , $V_{13}=5V$	2.2	2.6	3.0	V
	V <sub>9-2(2)</sub>	1	$S_1=B$ , $S_2=A$ , $V_{10}=4.5V$ , $V_{13}=5V$	0		0.2	V
Phase Comparison Ampli—	V <sub>22-2(1)</sub>	2	$S_3=B, S_4=B, S_5=B, V_1=2.2V$	28	31	35	V
fier Output Voltage	V <sub>22-2(2)</sub>	2	$S_3=B, S_4=B, S_5=B, V_1=3.2V$	0.4	1.0	1.6	V
BM Output Voltage	$V_{18-2}$	2	$S_3=A, S_4=B, S_5=A, V_1=0$	12.5	13.6	14.7	V
BU Output Voltage	V <sub>19-2</sub>	2	$S_3=A, S_4=C, S_5=C, V_1=0$	11.1	12.2	13.5	V
BV Output Voltage	$V_{20-2}$	2	$S_3=A, S_4=B, S_5=D, V_1=0$	11.1	12.3	13.5	V
DC Outside Walks as	V <sub>21-2(1)</sub>	2	$S_3=A, S_4=B, S_5=B, V_1=0$	0	0.4	0.8	V
BS Output Voltage	V <sub>21-2(2)</sub>	2	$S_3=A$ , $S_4=A$ , $S_5=B$ , $V_1=0$	26.5		27	V
V <sub>CC1</sub> Voltage Regulator	$V_{5-2}$	2	$S_3=A, S_4=B, S_5=B, V_1=0$	29	31.5	34	V
V <sub>CC1</sub> Operating Resistance	r <sub>5</sub>	2	$S_3=A$ , $S_4=B$ , $S_5=B$ , $V_1=0$		10	25	Ω

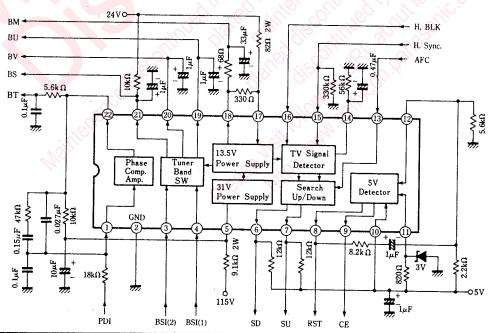
 $\textbf{Test Circuit 1} \ (I_{5,\,10,\,17},\ V_{6-2(1),\,(2),\,(3),\,(4)},\ V_{7-2(1),\,(2),\,(3),\,(4)},\ V_{8-2(1),\,(2)}\,,\ V_{9-2(1),\,(2)})$ 



 $\textbf{Test Circuit 2} \ \, (V_{22-2(1),(2)}, \ \, V_{18-2}, \ \, V_{19-2}, \ \, V_{20-2}, \ \, V_{21-2(1),(2)}, \ \, V_{5-2}, \ \, \mathbf{r}_{5} \, ) \\$ 



### Application Circuit



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